Five Level Reduce Switch Count Boost PFC Rectifier with Multicarrier PWM

Vinay Kumar[1], Vijay Anand Bharti[2]

[1]M.Tech, [2]Assistant Professor

Department of EE, Mittal Institute of Technology, Bhopal, India

Abstract— A multilevel boost PFC (Power Factor Correction) rectifier is presented in this paper controlled by cascaded controller and multicarrier pulse width modulation technique. The presented topology has less active semiconductor switches compared to similar ones reducing switching losses as well as the number of required gate drives that would shrink manufactured box significantly. A simple controller has been implemented on the studied converter to generate a constant voltage at the output while generating a five-level voltage waveform at the input without connecting the load to the neutral point of the DC bus capacitors. Multicarrier PWM technique has been used to produce switching pulses from control signal. Multi-level voltage waveform harmonics has been analyzed comprehensively which affects the size of input current and required filters directly. Full simulation and experimental results confirm the good dynamic performance of the proposed five-level PFC boost rectifier in delivering power from AC grid to the DC loads while correcting the power factor at the AC side as well as reducing the current harmonics remarkably.

Keywords— Packed U-Cell, PUC5, HPUC, Buck PFC rectifier, multilevel converter, power quality, CCM (Continuous Conduction Mode).

I. INTRODUCTION

Now a day's DC control supply is a major interest of ventures to energize batteries particularly for uninterruptible power supplies (UPS), electric vehicles (EV), and encouraging correspondence sheets and to use in different power applications [1]. Managed steady voltage at the yield in expansion to low consonant and solidarity control factor current at the information ought to be guaranteed in such hardware to agree to consonant norms characterized by various affiliations like IEEE furthermore, IEC [2, 3]. PFC rectifiers have been proposed numerous years prior to defeat the information AC voltage and current power Factor issue. Such converters can be separated into two fundamental classes dependent on their yield DC voltage abundancy. On the off chance that the yield DC voltage level is not exactly the info AC crest voltage esteem, it is known as a PFC buck rectifier and on the other hand, a PFC support rectifier produces a DC voltage more prominent than the AC crest voltage [4].

In a large number of electronic circuits, we require DC voltage for operation. We can easily convert the AC voltage or AC current into DC voltage or DC current by using a device called P-N junction diode.

One of the most important applications of a P-N junction diode is the rectification of Alternating Current (AC) into Direct Current (DC). A P-N junction diode allows electric current in only forward bias condition and blocks electric current in reverse bias condition. In simple words, a diode allows electric current in one direction. This unique property of the diode allows it to acts like a rectifier.

1.2 Background

In recent years many efforts are created to analysis and use new energy sources because the potential for an energy crisis is increasing. Multilevel converters have gained a lot of attention among the area of energy distribution and control due to their advantages in high power applications with low harmonics. They not only achieve high power ratings, but in addition modification the use of renewable energy sources. the general perform of the multilevel converter is to synthesize a desired high voltage from several levels of dc voltages which can be batteries, fuel cells, etc. [1, 2].

In an expansive number of electrical and electronic circuits, DC voltage is required for its activity. We can just adjust the AC voltage into DC voltage by utilizing a gadget called PN intersection diode. A standout amongst the hugest utilizations of a PN intersection diode is the amendment of AC into DC. A PN intersection diode grants electric flow in just a single heading i.e., forward predisposition condition and squares electric flow backward inclination condition. This single property of the diode gives it a chance to perform like a rectifier. This article talks about various sorts of rectifiers and its examinations.

1.3 Rectifier

A rectifier is an electrical gadget involves at least one diodes which permit the stream of flow just one way. It essentially changes over rotating current into direct current. Rectifiers can be form in a few shapes according to need like semiconductor diodes, SCRs (silicon controlled rectifiers), vacuum tube diodes, mercury bend valves, and so forth. In our past articles, we have clarified diodes, sorts of diodes in detail. In any case, in this, we will give subtle elements of rectifiers, sorts of rectifiers and its applications and so forth.

1.4 Multilevel Rectifier

Single-stage rectifiers are normally utilized for power supplies for household gear. Be that as it may, for most modern and high-control applications, three-stage rectifier circuits are the standard. Similarly as with single-stage rectifiers, three-stage rectifiers can appear as a half-wave circuit, a full-wave circuit utilizing an inside tapped transformer or a full-wave connect circuit. Thyristors are ordinarily utilized instead of diodes to make a circuit that can manage the yield voltage. Numerous gadgets that give coordinate current really create three-stage AC. For instance, a vehicle alternator contains six diodes, which work as a full-wave rectifier for battery charge.

In molecule quickening agents, electrons or other charged particles are compelled to move along circles or directions by methods for attractive fields. The force of the attractive fields expected to get the wanted impacts is identified with the vitality of the particles. Electromagnets, ordinary hot ones or superconducting ones, are regularly utilized. The excitation current in the magnets can run from a few amperes for little circle amendment loops to somewhere in the range of hundreds or thousands of amperes (see, for instance) The power converters expected to cover such a wide current range have generally varying structures and attributes and, for a similar power necessity, a few arrangements are regularly conceivable. In this paper I demonstrate the topologies and the qualities of a specific class of rectifiers—the line commutated ones—that was and still is broadly utilized in atom smasher offices. Indeed, even today, in the 'PWM Era', line commutated rectifiers are working. Additionally, Switch Mode Power Supplies (SMPS) regularly incorporate into their structure 'traditional' rectifiers as information or yield stages or both.

II. LITERATURE REVIEW

M. Mobarrez, et. al. [1] "A Novel Control Approach for Protection of Multi-Terminal VSC based HVDC Transmission System against DC Faults," By and large execution of the voltage source converters (VSCs) has enhanced amid the ongoing years. Enhancement of the VSCs alongside the alluring highlights of the VSC based high voltage coordinate current (HVDC) transmission frameworks over the thyristor based HVDC transmission frameworks make it conceivable to construct multiterminal (MT) VSC HVDC transmission frameworks. Be that as it may, the VSCs are powerless against dc side shortcomings and a technique should be utilized to smother the dc blame current. In this paper, three distinct setups of strong state dc circuit breakers (CB) for security reasons for existing are contemplated. Additionally, another control technique to ensure the VSCs against the dc side blame is proposed, the new strategy makes it conceivable to quench the dc blame current with the current air conditioning breakers on the air conditioner side or with the lower rating strong state (SS) DCCBs. The execution of the SS DCCBs and the proposed strategy are considered utilizing Real Time Digital Simulator (RTDS).

X. Wu, et. al. [2] "Variable on-time (VOT)-controlled critical conduction mode buck PFC converter for high input AC/DC HB-LED lighting applications," For high information voltage (>264 Va c) air conditioning/dc applications, a buck control factor rectification (PFC) converter is a decent decision on account of its low yield voltage, high proficiency, lifetime enhancement, furthermore, cost decrease by utilizing a low voltage rating (<200 V) electrolytic capacitor. Notwithstanding, because of the innate dead point of the info current, the music of the buck PFC converter are high, which restrains its application in lighting frameworks. With the end goal to make the buck PFC converter meet the music necessities (IEC61000-3-2, Class C) in lighting applications, this paper proposes a variable on-time controller for a basic conduction mode (CRM) buck PFC front-end converter for detached high-splendor Driven applications. By feed forwarding the information voltage and directing the on-time of the switch, the high-arrange sounds can be diminished to meet the lighting framework confinements. Exploratory results got on a 150-W CRM buck front-end PFC model demonstrate that the productivity of buck PFC surpasses 96% amid the whole line input run (250– 530 Va c) at full load, and the current music substance can meet the symphonious necessities.

Y. Jang, et. al. [3]"Bridgeless high-power-factor buck converter," A bridgeless buck control factor redress rectifier that significantly enhances proficiency at low line of the universal line go is presented. By dispensing with info connect diodes, the proposed rectifier's productivity is additionally made strides. Besides, the rectifier duplicates its yield voltage, which broadens useable vitality of the mass capacitor after a dropout of the line voltage. The task what's more, execution of the proposed circuit was confirmed on a 700-W, widespread line trial model working at 65 kHz. The estimated efficiencies at half load from 115 and 230 V line are both near 96.4%. The effectiveness distinction between low line and high line is under 0.5% at full load. A second-arrange half-connect converter was additionally included to demonstrate that the consolidated power stages effectively meet Climate Saver Computing Initiative Gold Standard.

X. Xie, C. et.al. [4] "An improved buck PFC converter with high power factor," An enhanced buck PFC converter topology is proposed in this paper. By including a helper switch and two diodes, the no man's lands in AC input current of customary buck PFC converter can be dispensed with. An enhanced steady on-time control is proposed and used in this enhanced buck PFC converter to drive it work in basic proceeds with conduction mode (CRM). With ideal control parameters, almost unit control factor can be accomplished and the info current sounds can meet the IEC61000-3-2 Class C standard inside the all inclusive input voltage go. In addition, the effectiveness of the proposed converter isn't crumbled contrasted with the ordinary buck converter. Nitty gritty hypothetical investigation and ideal plan contemplations for the proposed converter are introduced and confirmed by a 100-W lab-made model.

P. Chaudhary et.al. [5] "Front-end buck rectifier with reduced filter size and single-loop control," This paper introduces a transformer less answer for front-end amendment, which is especially appropriate for footing applications, requiring high voltages to be ventured down to suitable dc voltage. The proposed topology depends on pulse width modulation buck rectifier (current source inverter topology) and is equipped for correction and venturing down of single-stage air conditioning supply, in a solitary stage. Another control conspire is proposed to accomplish steady dc yield voltage and sinusoidal source current, independent of expansive swells in the dc inductor current. The proposed conspire is designed in single-circle voltage control mode. The pertinent little flag display is gotten from the vast flag demonstrate utilizing multi order deterioration. A detailed strategy of dc channel configuration is talked about, for circuit activity with least vitality stockpiling. Every single investigative outcome are approved by numerical reproduction for sinusoidal and contorted source voltage. Trial check is accomplished through a 1.2-kW matrix associated research center model.

D. Dai, S. et.al. [6] "Slow-scale instability of single stage power-factor-correction power supplies," This paper reports moderate scale unsteadiness in a single-arrange control factor-revision (PFC) control supply, which is a prevalent structure answer for low power applications. The circuit utilizes a course arrangement of a lift converter and a forward converter, which share a functioning switch and work in broken conduction mode (DCM), to give input PFC what's more, tight yield control. Primary outcomes are given by "correct" cycle-by-cycle circuit reenactments. The impact of the moderate scale insecurity on the achievable power factor is delineated as far as add up to symphonious twisting which can be found by taking the quick Fourier change of the information current. The moderate scale precariousness normally shows itself as neighborhood motions inside a line cycle. In light of the basic state of DCM for the buck converter, the fundamental instrument of such insecurity is additionally researched. It has been discovered that fringe crash is the hidden reason for the wonder. Additionally, it has been demonstrated that the fringe crash saw here is adequately a non smooth Neimark– Sacker bifurcation. At last, trial results are exhibited for confirmation Purposes.

L. Huber, et.al. [7] "Design-oriented analysis and performance evaluation of buck PFC front end," In all inclusive line air conditioning/dc converters that require control factor adjustment (PFC), keeping up a high productivity over the whole line and load ranges represents a noteworthy test. Normally, a help PFC front end shows 1%– 3% bring down proficiency at 100-V line contrasted with that at 230-V line. It is appeared in this paper a buck PFC front end with a yield voltage in the 80-V range can keep up a high productivity over the whole line and load ranges. An intensive investigation of the buck PFC converter activity and execution alongside structure improvement rules are displayed. Trial results acquired on a 90-W journal connector are given. A misfortune examination dependent on SIMPLIS and PSPICE recreations is additionally included. Central points that add to the progressed productivity of the buck PFC versus the lift PFC are quickly clarified.

T. Tanaka, et.al. [8] "Smart charger for electric vehicles with power-quality compensator on single-phase three-wire distribution feeders," In this paper, we propose a shrewd charger for electric vehicles with a power-quality compensator. The proposed shrewd charger comprises of four-leg protected entryway bipolar transistors (IGBTs). Three legs are utilized for a solitary stage full-connect based pulse width-balanced (PWM) rectifier, which changes over power from air conditioning to dc amid the battery-charging activity or from dc to air conditioning amid the battery-releasing activity. This PWM rectifier can remunerate responsive and uneven dynamic flows on single-stage three-wire conveyance frameworks in light of the fact that the third leg is associated with the nonpartisan line of single-stage three-wire conveyance feeders. The fourth leg is utilized as a bidirectional dc–dc converter for battery-charging and battery-releasing activities. The three-leg PWM rectifier utilizes just steady dc capacitor voltage control, which is usually utilized in dynamic power line conditioners. In this way, the creators have built up the least complex conceivable control strategy for a solitary stage control quality compensator. The fundamental rule of the proposed brilliant charger is talked about in detail and after that affirmed by advanced PC recreation utilizing PSIM programming. A model trial display is developed and tried. Trial results show that adjusted source flows are acquired on the optional side of the post mounted circulation transformer for battery-charging and battery-releasing tasks.

III. PROBLEM FORMULATION

Delivering diverse voltage levels lessens the voltage sounds which influences the network Current symphonious substance specifically. Low exchanging recurrence of the proposed rectifier is a recognized trademark among other buck type rectifiers that lessens exchanging misfortunes and any high exchanging recurrence related issues, fundamentally.

IV. METHODOLOGY

A reduced switch count 5-level boost PFC rectifier has been presented. Multicarrier PWM technique has been used to produce switching pulses from control signal. A simple controller has been implemented on the proposed converter to generate a constant voltage at the output while generating a five-level voltage waveform at the input without connecting the load to the neutral point of the DC bus capacitors.

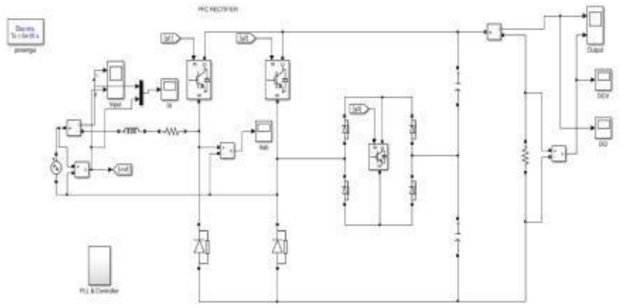

Figure 4.1 Proposed Modal

4.1 Introduction

The planned topology includes an ac-link that is common among the three phases. The ac-link provides three voltage levels +2E, 0, and -2E for the phase legs. Since all the phases have similar configuration, only one section leg of the planned topology. All the elements shown among the figure have equal operational voltage E i.e. one fourth of the ac-link voltage Vdc. The flying capacitors CA1 and CA2 are controlled to stay charged at the target voltage E.

The offered states of one phase leg are shown in table I. to get level 2E, the entire best arm switches SA1, SA2, SA3, and SA4 need to turn on. For level E, two decisions are offered i.e. either through ac-link's positive point (EP) or through ac-link's neutral point (E0). This redundancy is commonly used to balance the voltage of CA1. Level zero is generated through clamping the ac link's neutral point to the output (00). Negative states are typically generated equally due to the symmetry of the topology.

The operation of this topology is in essence the same as topologies like stacked multi cell (SMC) convertor where the positive and negative stacks operate severally. Hence, the positive stack capacitor CA1 is used and balanced throughout the positive cycle and rst throughout the negative cycle, whereas the negative stack capacitor CA2 is used and balanced throughout the negative cycle and rest throughout the positive cycle. So, the flying capacitors will see the switch frequency rather than line frequency and then the capacitor size is not large.

Similar to the three-level NPC rectifier, if the three phases of the load are balanced, the neutral purpose voltage is constant in theory. However, the voltage might slightly drift away due to the imbalance inside the elements' leak current. In addition, although small, there is always some imbalance among the phases. a constant voltage drifts, although very little can cause higher voltage across a part of the devices which can be lethal. However, this drift is also compensated by injecting very little common mode to the three phases. A vital feature of the planned topology is that the even distribution of transitions among modification devices. Therefore, modification loss that's that the key limiting issue of rectifier's thermal performance is distributed among the switches. As a result of the main result, the trade-off between switch frequency and current rating is improved. This provides the opportunity to either increase the rated current or power of the inverter or increases the switch frequency leading to lower capacitor size and improved voltage wave quality.

V. SIMULATION RESULTS

To verify the operation of the proposed topology and a model is developed and simulated with MATLAB software. The performance of the natural balancing technique for a three phase inverter.

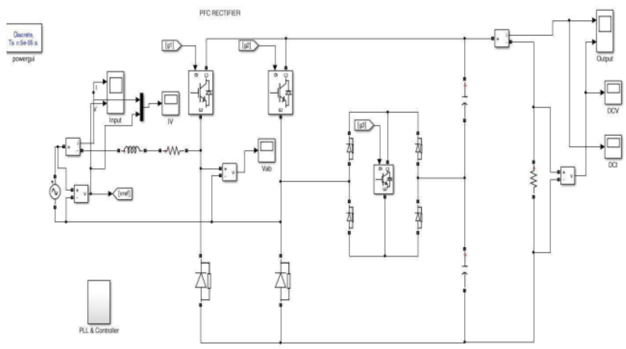

Figure 5.1 Model of the proposed system

TABLE II: SYSTEM PARAMETERS

AC Grid Voltage	120 V RMS
AC Grid Frequency	60 Hz
Interface Inductor	2.5 mH
DC voltages (Vdc)	200 V
DC Capacitor (C1 & C2)	1000 uF
DC Load (RL)	80 and 40
Switching Frequency	5 kHz

As represented in Fig. 5.2 four bearers (Cr1, Cr2, Cr3 and Cr4) are moved vertically to tweak the determined reference flag (Uref). Every transporter is dependable of creating beats for partner voltage level and exchanging states as appeared by rationale squares. Additionally, relating exchanging beats for three cycles of the regulated waveform (Uref) have been portrayed in Fig. 5 to show the settled exchanging recurrence in each cycle The proposed strategy guarantees low and settled exchanging recurrence usefulness of the 5-level converter goes for low exchanging misfortunes and high productivity contrasted with different topologies.

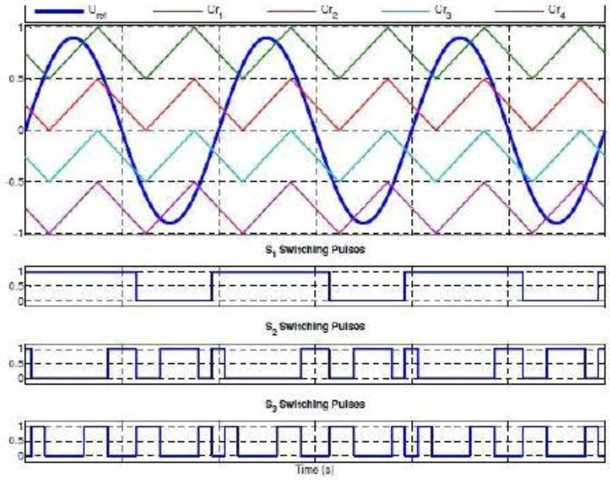

Figure 5.2 Proposed multicarrier PWM technique for low and fixed switching frequency purposes

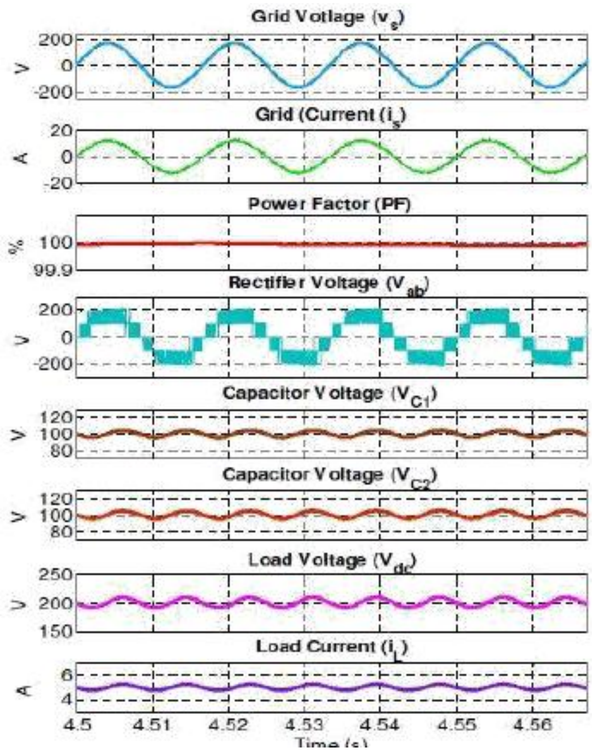

Figure 5.3 Simulation results from steady-state operation of the rectifier

At first the steady-state operation of the rectifier with implemented controller and change technique has been shown in Fig.5.3 the rectifier is fed from a 120V RMS grid while is *is* synchronic with vs making certain unity power issue mode of operation. 5-level voltage wave form at the input of the rectifier is illustrated therein figure containing low harmonic pollution that affects the grid current Doctor of Theology positively. 40 Load is connected

At the DC facet and Vdc fixed at 200V with acceptable ripple amplitude. VC1 and VC2 have been balanced and stuck at 100V and also the ripple frequency is 120Hz consequently. Additionally delineate that has small ripple obligatory by the load voltage.

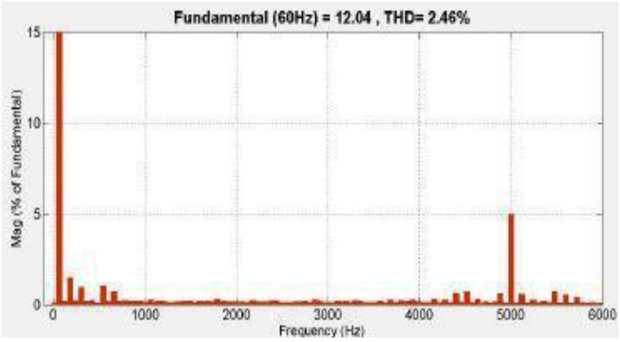

Figure 5.4 Harmonic Spectrum of grid current (is n)

is Waveform has been analyzed in terms of the harmonic elements and results are displayed in Fig.5.4 the THD is around a pair of 2.5% which is less than commonplace level (5%) Moreover, it should be noticed that the very best amplitude of harmonic orders is at five kHz that proves that the change frequency is fastened at the selected price victimization the multicarrier PWM technique.

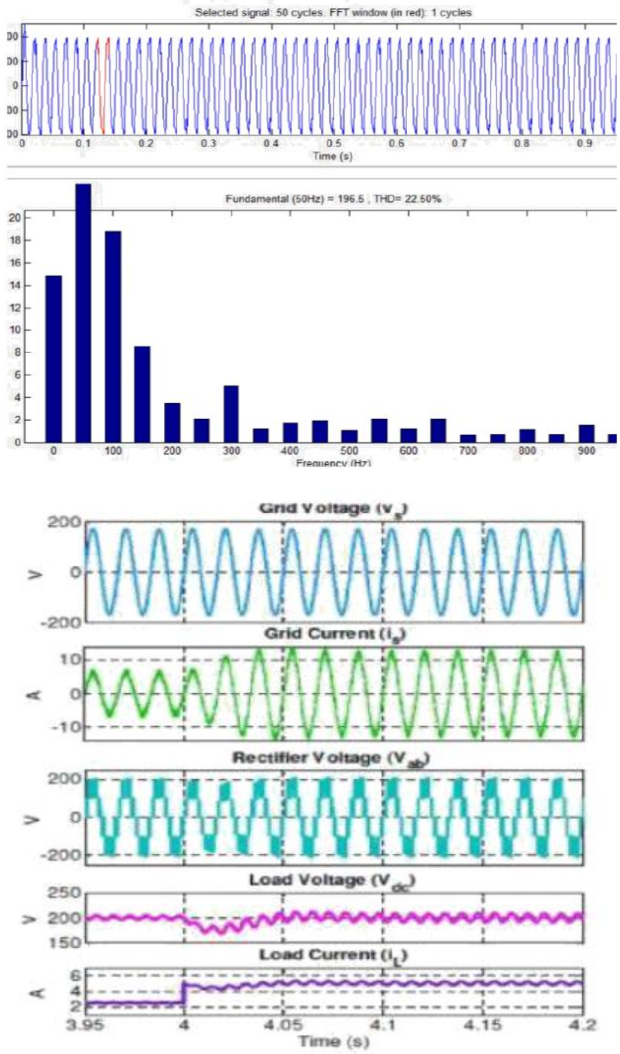

Figure 5.5 Simulation results during 50% decrease in the load

In order to simulate the transient modes, completely different tests have been performed to verify the great dynamic performance of proposed convertor with enforced controller. As shown in Fig.5.5 a five hundredth amendment within the load has been created suddenly which is that the most happening case in rectifier systems. Although is and *iL* square measure inflated because of reducing the load from 80 to 40 no result is determined on *vs* and *vdc* yet because the rectifier remains operating in unity power issue mode.

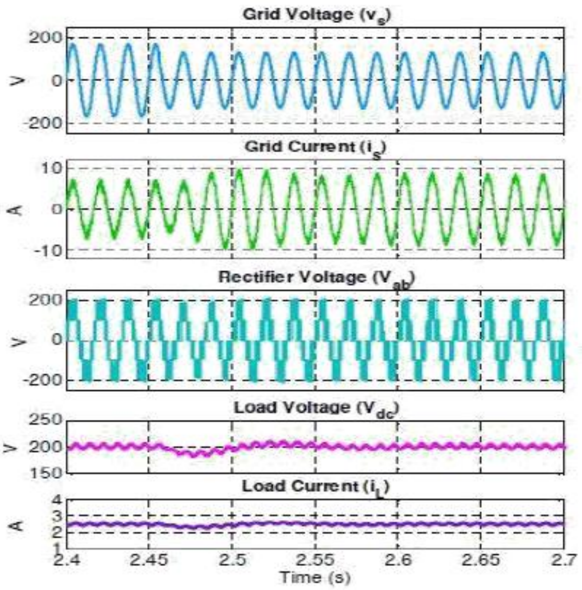

Figure 5.6 Simulation results during AC source voltage variation

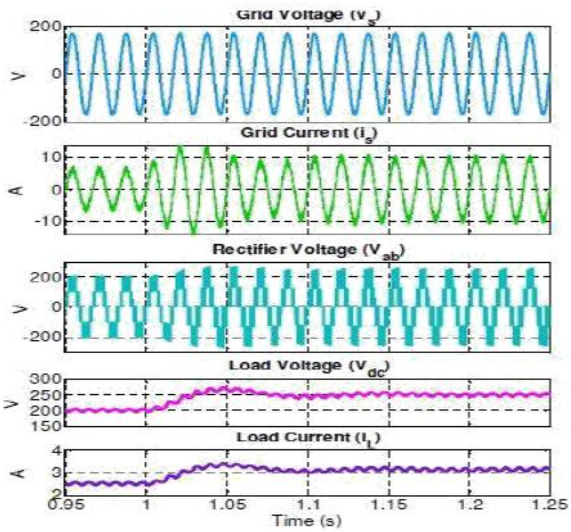

Figure 5.7 Simulation results during 25% raise in the DC voltage reference

The input AC voltage has been modified as an unwanted downside within the network. As illustrated in Fig. 5.6the DC aspect power consumption (*vdc iL*) isn't varied however vs has been reduced, so *is* is increased proportional to the power delivered to the load.

The last check, the DC voltage reference (Vdc*) has been accumulated by 25th from 200V to 250V to ascertain the tracking performance of the controller. Results are plotted in Fig.5.7 during which all values except vs are increased consequently. The controller half-track the new reference voltage price in but 0. 1*s* Is quickly.

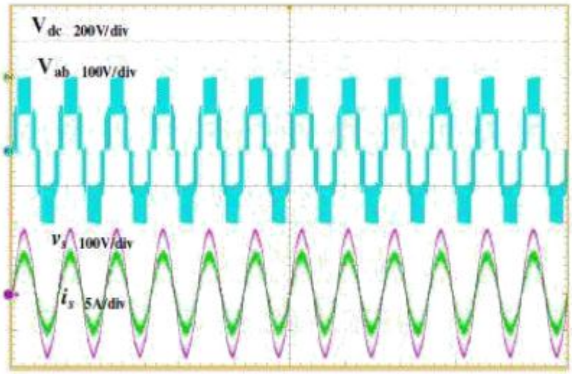

Figure 5.8 Experimental results of the 5-level rectifier and implemented Controller in steady state

In all simulation results, the 5-level voltage wave shape has been illustrated that confirms acceptable change actions at right times with none noises or unwanted pulses.

In order to validate all the simulation results, an experimental epitome of the conferred 5-level rectifier has been designed mistreatment three assail Mosfets of kind SCT2080KE and six quick recovery assail diodes of kind SCS220KG. The projected controller and modulation technique are enforced on a dSpace 1103 to get pulses for the associated switches. A gentle state result has been captured by the scope and illustrated in Fig.5.8 once the rectifier was generating 200V DC voltage at the output. The 5-level voltage wave form as well as low ripple DC voltage is obvious in this figure. Moreover, unity power issue operation of the rectifier is evident by the grid aspect voltage and current waveforms. The low harmonic AC current has been achieved by switch procedure of the 5-level rectifier mistreatment projected technique. Various conditions are applied on the running system to investigate the dynamic performance of the projected rectifier with enforced cascaded controller and switch technique. Low harmonic 5-level wave form of the rectifier as well as fastened switch frequency as Associate in Nursing action of mistreatment multicarrier technique create this work attention-grabbing for power industries.

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VI. CONCLUSION & FUTURE WORK

6.1 Conclusion

In this dissertation a 5-level rectifier working in buck mode has been proposed which is called HPUC as a slight change to PUC staggered converter. It has been exhibited that the proposed rectifier can delude the framework by producing most extreme voltage dimension of 250V at AC side as lift mode while part this voltage esteem at its two yield terminals to furnish buck method of activity with 125V DC useable for battery chargers or media transmission sheets' feeder. In spite of the fact that it has more dynamic switches than other buck rectifier topologies and a few impediments on power balance between loads, generally speaking framework works in lift mode and CCM which brings about evacuating massive AC and DC channels that generally utilized in customary buck PFC rectifiers. In addition, creating staggered waveform prompts diminished consonant segment of the voltage waveform and subsequently the line current. It moreover goes for working with low exchanging recurrence and little line inductor that all things considered describes low power misfortunes and high proficiency of the HPUC rectifier. Thorough hypothetical studies and reproductions have been performed on power adjusting issue of the HPUC rectifier. Rectifiers with satisfactory execution.

6.2 Future Work

Future work can be devoted to developing robust and nonlinear controllers on the proposed rectifier topology.

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