

Power Quality Improvement using UPQC: A Review

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Abstract—The extensive application of power electronic devices induces harmonics in the utility system which generates problems related to the quality of power delivered. Good Power Quality is immense important for both industrial and domestic sectors. Researchers have tried and implemented many useful technology for removing all the voltage and current related harmonic occurrence problems which in turn improves thequality of power delivered to the customers. In general these devices are classified as custom power devices UPQC is one such device which is capable of improving power quality by eliminating source as well as load harmonics. This paper presents a review on power quality improvement by employing Unified Power Quality Conditioner.

Index Terms—Unified power quality conditioner (UPQC), Power quality, power electronic converters, dual control strategy, harmonic compensation, voltage sag and swell compensation.

I. INTRODUCTION

Now a-days non-linear loads are exploited extremely and we are largely dependent on it. Non-linear loads are televisions, arc furnaces, printing and fax machines, microwave ovens, rectifiers, inverters, electronic gadgets, speed drives, AC, etc. All these non-linear loads introduce harmonics in the lines. The stability of any electrical device is largely governed by the supply voltage and current waveforms. If the fundamental waveform is sinusoidal, and its

Harmonics are sinusoidal too then these harmonics occurs in integral multiples of the fundamental waveform consequently supplied power is deteriote. Due to these harmonic distortion generated by nonlinear loads several problems are caused in the appliances used in our purpose like: overheating of motor, increase in losses, permanent damage of equipment is the worst case, high error in meter reading, etc. Hence mitigation of harmonics both loads side or source side is a big challenge for a power engineer. Due to the harmonics introduction in the

Lines by the nonlinear loads other problems of are generated

such as voltage swell, voltage sag, flicker occurring in voltage, etc. consequently efficiency of power supply degrades.

In past, passive filters using tuned LC components were

in very much use to improve the quality of power by removing voltage and current harmonics. But its use is limited now a days since they have high cost, resonance, large size. The above mentioned problems can be resolved by the use of

Active power filter (APF) which has been in trend now a days.

Numerous topologies for the APF [1] - [17], shunt [18] - [24], series [25] and hybrid active power filters (APFs) [26] are available in literature which has been classified in Figure 1.



Figure-1 Classification of Active power filters.



Figure 2 Classification of UPQC

UPQC-R	Right shunt UPQC
UPQC-L	Left shunt UPQC
UPQC- I	Interline UPQC
UPQC-	Multi converter UPQC
MC	
UPQC-	Modular UPQC
MD	
UPQC-ML	Multi-level UPQC
UPQC-D	Distributed UPQC
UPQC-DG	Distributed generator integrated with
	UPQC
UPQC-P	Active power controlled UPQC
UPQC-R	Reactive power controlled UPQC
UPQC-	Minimum VA loading in UPQC
VAmin	
UPQC-S	UPQC mitigates both active power and
	reactive power

Table-1 Nomenclature for various types of UPQC

II. UNIFIED POWER QUALITY CONDITIONER

In general series and shunt APF are in use. Combining both series APF & shunt APF custom power device known as UPQC can be designed. UPQC is capable of eliminating both the voltage and current based distortions simultaneously.

A Shunt APF eliminates all kind of current problems like current harmonic compensation, reactive power compensation, power factor enhancement. A Series APF compensates voltage dip/rise so that voltage at load side is perfectly regulated. The Shunt APF is connected in parallel with transmission line and series APF is connected in series with transmission line. UPQC is formed by combining both series APF and shunt APF connected back to back on DC side.

In this controlling techniques used is hysteresis band controller using "p-q theory" for shunt APF and hysteresis band controller using Park's transformation or dq0 transformation for series APF. Figure 2 presents the general structure of UPQC which comprises of source supplied by and renewable energy source or by the utility source two back-back inverters may be current source inverters, voltage source inverters, Multi-level inverters etc. coupled through dc-link voltage. One inverter is connected in parallel through shunt impedance and is connected in series with help of series impedance. One inverter is connected source side another inverter is connected load side. Load may be linear, non-linear, single phase, three phase etc.

The basic topology has been evolved by researchers in numerous topologies to obtain the desired application. The classification various topologies of UPQC proposed in the literature has been classified in Figure 2.



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Fig-2 Basic structure of UPQC.

The control of dc-link voltage assumes a vital part in accomplishing the coveted UPQC execution. Amid the framework dynamic conditions, for instance, sudden load change, voltage droop, the dc-connect input controller ought to react as quick as conceivable to reestablish the dcinterface voltage at set reference esteem, with least deferral and additionally bring down overshoot. In literature various control strategies are available to tune the dc-link voltage and to achieve the desired controls for PQ issues which has been discussed in brief here.

UPQC-R and UPQC-L; Right and Left shunt UPQC, since UPQC consist of two converters either can be shunted, so when right converter is shunted its termed as UPQC-R as shown in Fig-2 and when left converter is shunted it is termed as UPQC-L. As shown in Fig-3.In UPQC-R, the current(s) that move through arrangement transformer is (are) mostly sinusoidal independent to the idea of load current on the framework (given that the shunt inverter repays current music, responsive current, unbalance, and so forth, successfully). In this way, UPQC-R gives a superior general UPQC execution contrast with UPQC-L. The UPQC-L structure is here and there utilized as a part of unique cases, for instance to avoid the interference between the shunt inverter and passive filters.



1) UPQC-I; When the two inverters of the UPQC are interlinked between twofeeders the resulting configuration is known as interline UPQC (UPQC-I). Among the two inverters one is connected in series and another in parallel with the two feeders of the distribution network. This

III. TOPOLOGIES OF UPQC

configuration assists the control of voltage of both the feeders simultaneously.

Also UPQC-I is capable to control the flow of active power among the feeders. Fig-4



2) UPQC-MC; third converter is furthermore connected in the existing UPQC configuration to support the Dc bus voltage is known as UPQC-MC (Fig-5). The third converter can either be connected in series or in parallel with one of the converter and with the feeder.



3) UPQC-MD; the cascaded H-Bridge MLI is used as the two converter to design modular UPQC topology as shown in Fig-6. In [27] and [28], the H-bridge modules for shunt part of UPQC are connected in series through a multi-winding transformer, while the H-bridges in the series part are directly connected in series and inserted in the distribution line without aseries injection transformer.



Fig-6UPQC-MD

4) UPQC-ML; to design UPQC-ML neutral point clamped MLI is used in place of series and shunt inverter as shown in Fig-7.



5) UPQC-D; the UPQC used to resolve PQ issues in three phase three wire and three phase four wire distribution network is called as UPQC-D as shown in fig-8. A 3P4W distribution system is generally realized by providing a neutral conductor along with the three power lines from substation or by utilizing a delta–star transformer at the distribution level [29-30].

6) UPQC-DG; UPQC employed to integrate one or more distributed generations like solar, wind, etc. with the utility grid is called UPQC-DG. The DG power can be regulated and managed through UPQC to supply to the loads connected to the PCC in addition to the voltage and current power quality problem compensation [31-36]. This topology of UPQC is very popular among the researchers of renewable energy resources and being widely used to mitigate the PQ issues like current harmonics, voltage flickers, real and reactive power compensation, etc. Fig-9 presents the topology for UPQC-DG.



Fig-9 UPQC-DG

IV. CONCLUSION

Degradation of power quality due to increased penetration of DERs and power electronic devices concern to improve the quality is also increasing to meet the grid code. Various custom power devices are available literature to enhance the quality of power or to mitigate PQ issues. This paper presents the review on UPQC to mitigate PQ issues. Available topologies its characteristics and classification of UPQC is presented.

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