

A Review on Various Shunt Active Filters

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Abstract- In this review paper we have studied various techniques of power filters designed for large-power applications, where complex digital control circuit and expensive batteries are often used. In this study, a simple and low-cost active power filters circuit using an analog-based hysteresis current controller and capacitive energy storage. The filter is intended to be a low-power add-on unit to reduce the AC harmonic currents of existing electronic equipment (e.g., personal computers), which impose nonlinear loads to the AC mains. The operation principle, design criteria, and control strategy of the proposed filter are discussed.

Keywords: Power Active Filters.

I. INTRODUCTION

The application of power electronic in power conversion shows drawbacks that lead to power quality problems which could relate to harmonics affecting communication interference, heating, solid-state devices malfunction, resonance. Solutions involve several techniques that include the use of passive and active power filter (APF). A more advance approach is the use of hybrid filters amongst other involves the use of both the passive filter and shunt APF in combination. They are being used to eliminate both the lower order and higher order harmonics. The passive filter is normally designed to eliminate the bulk of load-current harmonic leaving the more complex problems to be solved by the APF.

Shunt APF normally operates using pulse width modulation (PWM) inverter techniques to inject the required non-sinusoidal current requirements of nonlinear loads but is complex with the number of switches in use. Another approach is the use of series active power filter that uses basic bridge-diode circuit, boost circuit and an inductor. Single phase converter produces a relatively high proportion of ac ripple voltage at its dc terminal; it is undesirable because of its heat producing effect. A smoothing needed to get continuous operation. It can be minimized by increasing number of pulses. Three phase ac supply with a suitable transformer connection permits an increasing the pulse

number. When the number of pulses increased out put voltage gets smoothen. So here we are implementing an extension of single phase hybrid active power filter with three phases HAPF.

II. SHUNT ACTIVE POWER FILTER

The shunt topology is more popular as compared to others due to its performance and easy implementation. The shunt APF injects harmonic current in opposition to the load harmonics so as to cancel the effect of harmonics in the system as shown in fig. 1. The instantaneous power ($p-q$) theory is used for current reference generation and is implemented in simulink.

Hybrid active filters have been presented in the literature as a lower cost alternative to purely active filters for harmonic compensation. Hybrid filters use combinations of passive elements to reduce the ratings of the active element required, [1-4]. The development of hybrid filter topologies has followed a natural progression from purely passive tuned filters which have been used for many years, to simplified passive structures with one active element. These passive structures have been largely based on the original tuned structures. This paper systematically reviews the possible alternatives for hybrid active filter topologies and identifies those most suitable for harmonic reduction with low active component ratings. The method used is to:

- select suitable models for the load
- determine the desirable attributes of a filter
- generate possible topologies and identify useful filters

This approach uncovers existing and new topologies and establishes a systematic framework for describing active filters.

III. LITERATURE SURVEY

Chi-Seng Lam; Wai-Hei Choi; Man-Chung Wong; Ying-Duo Han [1] in 2012 presents the reference dc-link voltage is classified into certain levels for selection in order to alleviate the problem of dc voltage fluctuation caused by its reference

frequent variation, and hence reducing the fluctuation impact on the compensation performances. Finally, representative simulation and experimental results of a three-phase four-wire center-split LC -HAPF are presented to verify the validity and effectiveness of the proposed adaptive dc-link voltage-controlled LC-HAPF in dynamic reactive power compensation.

Patel, P.; Mulla, M. A. [2] in 2012 worked on most common problem arising in supply network is current harmonics because of the increased use of nonlinear loads. Traditionally passive and active filters are best solutions for harmonic removal, but they have some practical limitations. Solution to these limitations is to use hybrid active power filters (HAPF) which are different combinations of passive and/or active filters. HAPF inherit the efficiency of passive filters and the improved performance of active filters. In HAPF, filtering characteristics of passive filter is improved by active power filter. Three different types of HAPF are discussed in this paper. All topologies are presented and are verified via simulation study in PSIM software. Performance parameters of all the topologies are compared based on supply current THD, load voltage THD and fundamental voltage/current values for similar supply and load conditions.

Lam, C.-S.; Wong, M.-C.; Han, Y.-D [3] in 2012 presented a hysteresis pulse width modulation study for a inductor-capacitor (LC)-coupling hybrid active power filter (HAPF). As the coupling LC impedance yields a non-linear inverter current slope, this can affect the controllability of using the conventional hysteresis control method and generate unexpected trigger signals to the switching devices. This results in deteriorating the system operating performances. The design criteria of hysteresis band and sampling time can then be derived. Single-phase simulation and experimental results are given to verify the hysteresis control study of HAPF compared with active power filter.

Bhattacharya, A.; Chakraborty, C.; Bhattacharya, S.[4] in 2012 proposed a combination of low- and high-frequency hybrid active power filter (APF) to operate in parallel for better performance. The individual hybrid APF is a series combination of L-C filter with the corresponding voltage source inverter. The dc links of both the inverters are connected in parallel, and the voltage of the dc link is maintained by the low-frequency inverter (LFI). The low- and high-frequency inverters eliminate lower order and higher order harmonics, respectively. In addition, it is possible to design the LFI such that it can also compensate the reactive power of the load. The individual L-C filter of

the hybrid topology is designed to take care of specific order of harmonics that are predominant in the load. A combination of feedforward and feedback controller is designed for the proposed conditioner.

Mulla, M.A.; Patel, P.; Chudamani, R.; Chowdhury, A.[5] in 2012 done research on power quality problems have received increasing attentions in recent years due to proliferation of nonlinear and sensitive loads as well as unpredictable and unavoidable system faults. Power quality problems mainly include current harmonics, reactive power, supply unbalance, sag, swell etc. Series Hybrid Active Power Filters (SHAPF) has been the main topic of interest for researchers working in the area of active power filters (APF). SHAPF provides cost-effective solution as compared to standalone active filters and are preferred to compensate both voltage and current type harmonics. This paper present simplified control strategy for SHAPF with added functionality of compensating voltage sag, swell and unbalance. Mathematical formulation of reference generation scheme with simulation verification of SHAPF model is tested for compensation of voltage sag, swell, unbalance and harmonics is presented.

Wai-Hei Choi; Chi-Seng Lam; Man-Chung Wong; Ying-Duo Han, [7] in 2013 investigated different dc-link voltage control strategies in a three-phase four-wire LC coupling hybrid active power filter (LC -HAPF) for reactive power compensation. By using direct current (current reference) pulsewidth modulation (PWM) control method, to achieve dc-link voltage self-charging function during LC -HAPF start-up process, the dc-link voltage control signal feedback as reactive current component is more effective than the traditional method as an active current component.

IV. PROPOSED METHODOLOGY

The new proposed hybrid APF consists of two types of filter; simple LC passive filter and a PAPF for removing both high order and low order harmonic components. Fig. 1 shows the arrangement of proposed hybrid APF and in block diagram of Fig. 2 with its control components of the PAPF is used to inject equal but opposite current into the system to mitigate the distortion current to a sinusoidal form; in phase and time with the voltage supply. The new proposed PAPF topology only consist a single active power switch (IGBT) in order to simplify the compensation circuit and reduce the switching stress.

The SCCL (fig.1) is used to monitor the supply current waveform and make corrections by current compensation

techniques. If the supply current is distorted, the SCCL will respond by providing switching signal to the IGBT that will inject the current compensation from the PAPF circuit to the mains to compensate the distorted supply current into a sinusoidal form. Unipolar switching is proposed due to the use of one power switch in the system. The compensation circuit uses a boost and PWM technique to generate the injected current into the system.

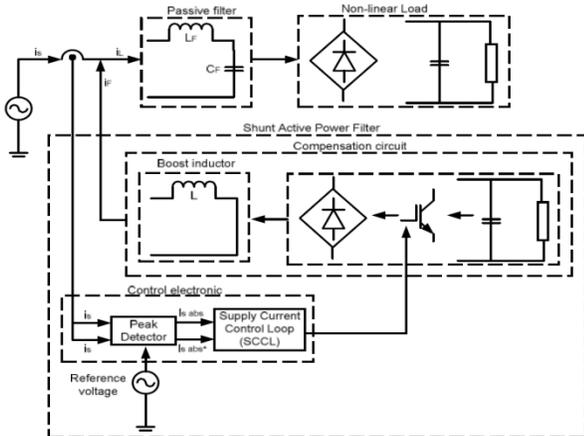


Fig. 1: The Proposed Hybrid APF Arrangement

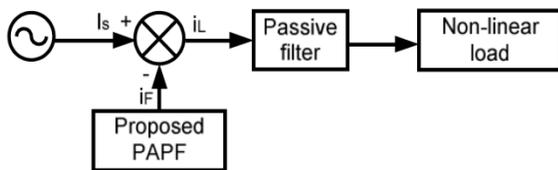


Fig. 2: Functional Block Diagram of Hybrid APF

In this work, active PWM as active current wave shaping is used for switching control. This technique allows active comparison of the error signal with the carrier signal to ensure error is kept within the boundaries of the carrier peaks at all times. The active PWM operates by comparing the corrected signal with the carrier signal to produce the required PWM control. When sinusoidal signal has magnitude larger than or equal to the carrier signal, the comparator output (PWM sequence) is higher. A proportional integral (PI) control algorithm is used to regulate the error.

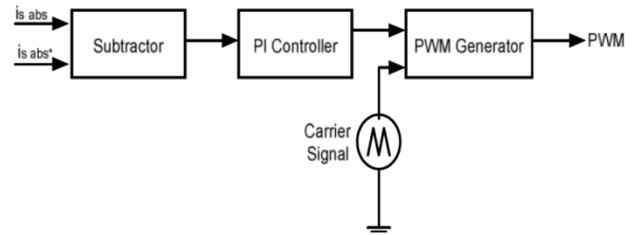


Fig. 3: Control Components of PAPF

The operation of the PAPF is best described by the illustration as shown in Figure 4 to inject the required current into the system. When switch is turned ON (Figure 4(a)), diode D_5 is reversed biased. Thus the output stage is isolated from the system. The input supplies energy to the boost inductor that causes inductor current to increase linearly with ramp behaviour. The energy stored in the inductor can be used for compensation purposes. When the switch is turned OFF, diode D_5 is forward biased as shown in the equivalent circuit of Fig. 4(b). There exists a change in current. Since the inductor cannot change instantaneously, the voltage in the inductor reverses its polarity to maintain constant current. In this stage, the current will flow through the inductor, diode D_5 and the compensating load.

V. CONCLUSION AND FUTURE SCOPE

The introduced mathematical tool proposes a general alternative to calculate the instantaneous power, based on a formal definition regardless of the reference frame and phases number of the power system. This new formulation allows the estimate of the reference currents for APFs control from a geometric point of view. This is accomplished by measuring of the shape deviations (changes in the dimensions) in each component of the power tensor proposed. Besides, an active power filter can be implemented with the simulation that has been studied.

REFERENCES

- [1] Chi-Seng Lam; Wai-Hei Choi; Man-Chung Wong; Ying-Duo Han, "Adaptive DC-Link Voltage-Controlled Hybrid Active Power Filters for Reactive Power Compensation," *Power Electronics, IEEE Transactions on*, vol.27, no.4, pp.1758,1772, April 2012.
- [2] Patel, P.; Mulla, M. A., "A comparative study on different types of hybrid active power filters," *Engineering Education: Innovative Practices and Future Trends (AICERA), 2012 IEEE International Conference on*, vol., no., pp.1,6, 19-21 July 2012.

- [3] Lam, C.-S.; Wong, M.-C.; Han, Y.-D., "Hysteresis current control of hybrid active power filters," *Power Electronics, IET*, vol.5, no.7, pp.1175,1187, August 2012.
- [4] Bhattacharya, A.; Chakraborty, C.; Bhattacharya, S., "Parallel-Connected Shunt Hybrid Active Power Filters Operating at Different Switching Frequencies for Improved Performance," *Industrial Electronics, IEEE Transactions on*, vol.59, no.11, pp.4007,4019, Nov. 2012.
- [5] Mulla, M.A.; Patel, P.; Chudamani, R.; Chowdhury, A., "A simplified control strategy for Series Hybrid Active Power Filter that compensates voltage sag, swell, unbalance and harmonics," *Power Electronics (IICPE), 2012 IEEE 5th India International Conference on*, vol., no., pp.1,6, 6-8 Dec. 2012.
- [6] Demirdelen, T.; Inci, M.; Bayindir, K.C.; Tumay, M., "Review of hybrid active power filter topologies and controllers," *Power Engineering, Energy and Electrical Drives (POWERENG), 2013 Fourth International Conference on*, vol., no., pp.587,592, 13-17 May 2013.
- [7] Wai-Hei Choi; Chi-Seng Lam; Man-Chung Wong; Ying-Duo Han, "Analysis of DC-Link Voltage Controls in Three-Phase Four-Wire Hybrid Active Power Filters," *Power Electronics, IEEE Transactions on*, vol.28, no.5, pp.2180,2191, May 2013.
- [8] Zobia, A.F., "Optimal multiobjective design of hybrid active power filters considering a distorted environment," *Industrial Electronics, IEEE Transactions on*, vol.61, no.1, pp.107,114, Jan. 2014.
- [9] A.M. Omar; "The Three-Phase Single Stage Flyback Converter", Doctor of Philosophy thesis, University of Malaya, Nov. 2001.
- [10] Moran L., Lpastorini J. D., and Wallace R., "Series active power filter compensates current harmonics and voltage unbalance simultaneously", *IEE Proc. Gener. Transm. Distrib.*, Vol. 147, No. 1, 2000.
- [11] El-Habrouk M., Darwish M. K., and Mehta P., "Active power filters: A review", *IEE Proceedings Electric Power Applications*, Vol. 147, No. 5, 2000, pp. 403-413.
- [12] Salam Z., Tan P. C., and Jusoh A., "Harmonics mitigation using active power filter: A technological review", *Elektrika*, Vol. 8, No. 2, 2006, pp. 17-26.
- [13] Singh B., Al-Haddad K., and Chandra A., "A review of active filters for power quality improvement", *IEEE Transactions on Industrial Electronics*, Vol. 46, No. 5, 2002, pp. 960-971.
- [14] Turunen J., Salo M., and Tuusa H., "A new approach for harmonic filtering in high power applications", *The Fifth International Conference on Power Electronics and Drive Systems, PEDS*, 2003, Vol. 2, pp. 1500- 1505.
- [15] Peng F. Z., "Application issues of active power filters", *IEEE Industry Applications Magazine*, Vol. 4, No. 5, 1998, pp. 21-30.
- [16] Akagi H., Srianthumrong S., and Tamai Y., "Comparisons in circuit configuration and filtering performance between hybrid and pure shunt active filters", *Conference Record of the Industry Applications Conference, 38th IAS Annual Meeting*, 2003, Vol. 2, pp. 1195-1202.
- [17] Al Zamil A. M., and Torrey D. A., "A passive series, active shunt filter for high power applications", *IEEE Transactions on Power Electronics*, Vol. 16, No. 1, 2002, pp. 101-109.
- [18] Benchaita L., Salem Nia A., and Saadate S., "Comparative study of two structures of shunt active filter suppressing particular harmonics", *The European Physical Journal Applied Physics*, Vol. 3, No. 1, 1998, pp. 59- 69.
- [19] Bhattacharya S., Po-Tai C., and Divan D. M., "Hybrid solutions for improving passive filter performance in high power applications", *IEEE Transactions on Industry Applications*, Vol. 33, No. 3, 1997, pp. 732- 747.
- [20] Fujita H., and Akagi H., "Design strategy for the combined system of shunt passive and series active filters", *Conference Record of the 1991 IEEE Industry Applications Society Annual Meeting*, 1991, pp. 898-903 vol.891.
- [21] Man-Chung W., Chi-Seng L., and Ning-Yi D., "Comparison of structure topologies for hybrid filters", *43rd International Universities Power Engineering Conference, UPEC* 2008, pp. 1-5.